

one, IP5389 Selection instructions
1, supported lamp/188 Digital Tube pin Selected model

Модель IP5389	Selection reference	Number of functions
IP5389_BZ	1, mainly used for belts led and digital tube 2, support A+A+C+B+Lightning interface plan	1, support led and 188 Automatic Identification 2, support 2-6S Cell string number selection 3, support pin Select power, full voltage, NTC and other functions 4, support SCP Protocol input and output
IP5389_BZ_2H	1, mainly used for belts led and digital tube 2, support A+A+C+B+Lightning Interface scheme 3, support "always on" mode, press and hold the button 2s Turn on, keep discharging 2h Do not shut down.	1, support led and 188 Automatic Identification 2, support 2-6S Cell string number selection 3, support pin Select power, full voltage, NTC and other functions 4, support SCP Protocol input and output
IP5389_BZ_AACC	1, mainly used for belts led and digital tube 2, support A+A+(DRP)+ C(DRP) pair C Interface scheme	1, support led and 188 Automatic Identification 2, support 2-6S Cell string number selection 3, support pin Select power, full voltage, NTC and other functions 4, support SCP Protocol input and output
IP5389_BZ_AACC_2H	1, mainly used for belts led and digital tube 2, support A+A+C+B+Lightning interface plan 3, support "always on" mode, press and hold the button 2s open Turn on, keep discharging 2h Do not shut down.	1, support led and 188 Automatic Identification 2, support 2-6S Cell string number selection 3, support pin Select power, full voltage, NTC and other functions 4, support SCP Protocol input and output
IP5389_BZ_ABCCO	1, mainly used for belts led and digital tube 2, support A+C(DFP)+B+ C(DRP) catch oral plan	1, support led and 188 Automatic Identification 2, support 2-6S Cell string number selection 3, support pin Select power, full voltage, NTC and other functions 4, support SCP Protocol input and output
IP5389_FPP_AACC	1, Support high-voltage fast charging, charge and discharge at the same time (C1 Quick mouth While charging the input C2 port fast charging output; C2 port fast charge input at the same time C1 port fast charging output; C1 or C2 Port fast charge input at the same time A port fast charging output) 2, support C(DRP)+C(DRP)+A+A pair C(DRP) Interface scheme	1, support led and 188 Automatic Identification 2, support 2-6S Cell string number selection 3, support pin Choose full voltage, NTC and other functions 4, support SCP Protocol input and output
IP5389_OCV_AACC	1. Mainly used with LED and digital tube 2. Supports A+A+(DRP)+C(DRP) double C interface solution 3. Support high-power lighting (requires external LED lights)	1, support led and 188 Automatic Identification 2, support 2-6S Cell string number selection 3, support pin Select power, full voltage, and other functions. Not supported yet NTC Function pin select.

	driver IC), and supports lightning current sampling	4,support SCP Protocol input and output
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The protocols supported by each port of the above scheme are as follows:

Input and output port	Supported fast charging protocols
USB A port output	QC2.0, QC3.0, FCP, AFC, low pressure SCP, low pressure VOOC
Micro B mouth input	AFC, high pressure SCP, FCP
Type-C mouth input	PD, AFC, high pressure SCP, FCP
Type-C port output	PD, QC2.0, QC3.0, FCP, AFC, high pressure SCP, low pressure VOOC
Lightning mouth input	PD(Highest 12V)

Among the above solutions, only IP5389_BZ and IP5389_BZ_2H of C port output, A Port output additional support QC3 + protocol.

2, support I2C Controlled model

IP5389 model	Selection reference	Number of functions
IP5389_I2C_AACC	1. Mainly used for coordination MCU Use, can pass Pass I2C access internal information; 2. Support supportA+A+(DRP)+ C(DRP) pair C Interface scheme	1, support fuel gauge readability 2, support 2~6s Cell string number selection 3, accessible I2C Customize various functions, such as Input and output power, PDO Current envelope Interest and so on 4, only supports SCP Protocol output
IP5389_I2C_BC	1, mainly used to cooperate with MCU Use, can pass Pass I2C access internal information; 2, support 1 individual C(DRP) Two-way input and output out; 3, VIN oral interface DC Power input supports solar input, DCDC adapter input, support MPPT, adaptive adapter output capability and other functions able	1, support fuel gauge readability 2, support 2~6s Cell string number selection accessible I2C Customize various functions, such as Input and output power, PDO Current envelope Interest and so on 3, only supports SCP Protocol output
IP5389_I2C_AACD	1, mainly used to cooperate with MCU Use via I2C access internal information; 2, support A+A+ C (DRP) C Oral two-way transmission Input and output plan; 3, VIN oral interface DC Power input supports solar input, DC DC adapter input, support MPPT, adaptive adapter output capability and other able functions	1, support fuel gauge readability 2, support 2~6s The number of cell strings can be selected through I2C Customize various functions, such as input Input and output power, PDO Current packet information, etc. 3, only supports SCP Protocol output
IP5389_I2C_DC_IP65	1, mainly used to cooperate with MCU Use via I2C access internal information; 2, can be plugged in IP6538, IP6525S Waiting for the bus Charge IC To achieve the effect of dual fast charging (only supports Use our car charger IC, specific support is fast The charging protocol is determined by the car charger used IC Decide) 3, support Lightning+B+C(DRP)+2A or A+C(DFP) Interface scheme	1, support fuel gauge readability 2, support 2~6s The number of cell strings can be selected through I2C Customize various functions, such as input Input and output power, PDO Current packet information, etc. 3, only supports SCP Protocol output

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Micro B mouth input	AFC, FCP
Type-C mouth input	PD, AFC, FCP
Type-C port output	PD, QC2.0, QC3.0, FCP, AFC, high pressure SCP, low pressure VOOC
Lightning mouth input	PD (Highest 12V)

3, can be added DC-DC model

IP5389 model	Selection reference	Number of functions
IP5389_DC_IP65	1, can be plugged in IP6538, IP6525S Waiting for car charging IC To achieve the effect of double fast charging (only supports our company car charger IC, the specific supported fast charging protocol is determined by the user car charger IC Decide) 2, support Lightning+B+C(DRP)+A+A or A+C(DFP) The interface solution supports "normally open" mode, long press the button 2s Turn on, keep discharging 2h Do not shut down. 3, Support smart power and high power mode selection, Pass resistor pin select	1, Support dual-port simultaneous fast charging output, independent of each other support led and 188 Automatic Identification 2 ,support 2-6S Cell string number selection 3, support pin Select power, full voltage, NTC and other functions 4, support SCP input Output

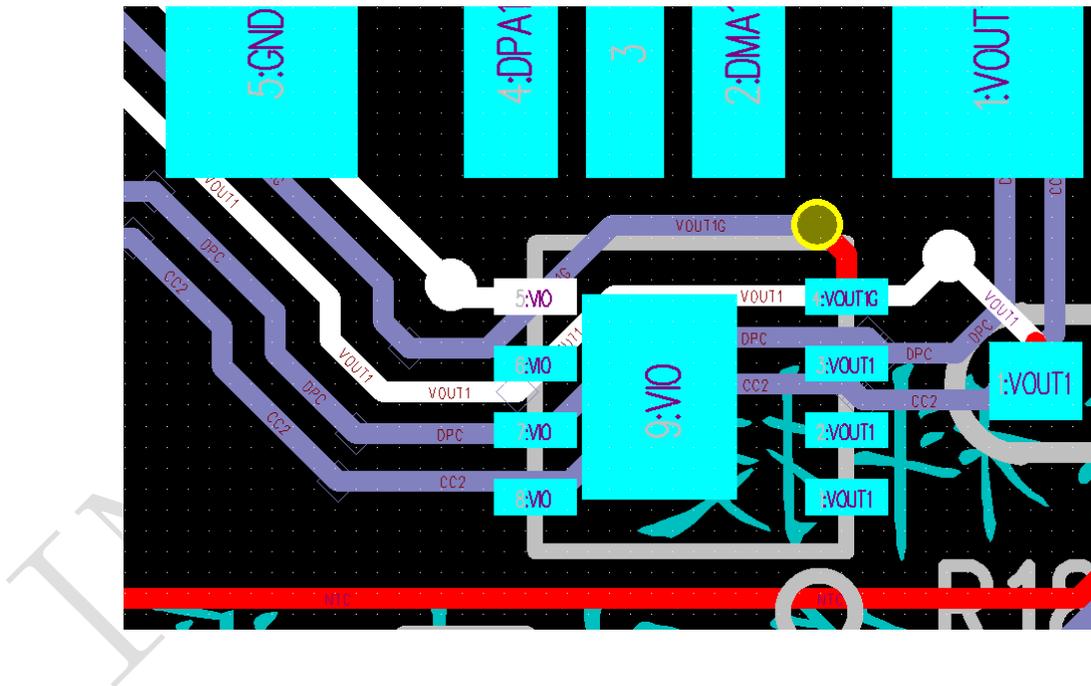
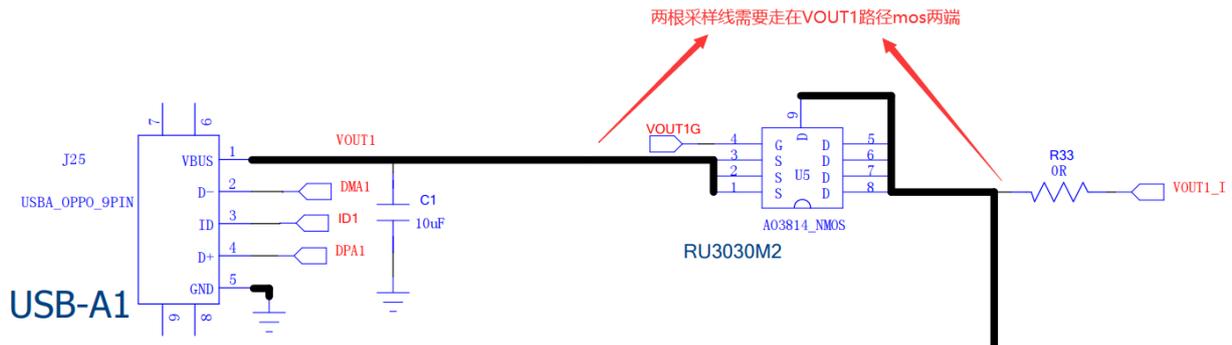
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Type-C mouth input	PD, AFC, high pressure SCP, FCP
Type-C port output	PD, QC2.0, QC3.0, FCP, AFC, high pressure SCP, low pressure VOOC
Lightning mouth input	PD (Highest 12V)

two,IP5389 layoutLayout suggestions

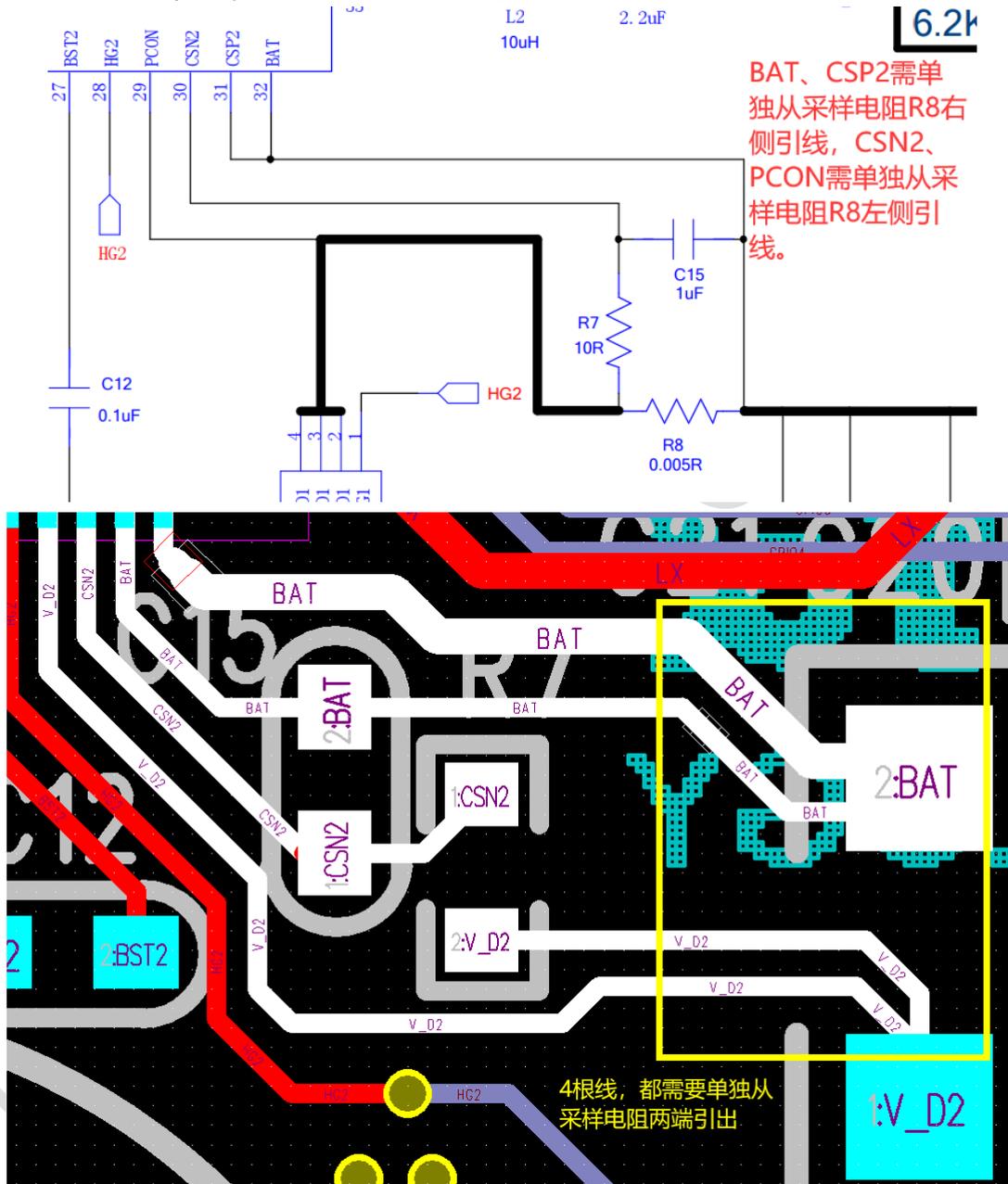
1.Current sampling wiring of each output port

existIP5389, each output and input port has two sampling lines toVOUT1Taking the port as an example, the two sampling lines are named VOUT1andVOUT1_I. When routing, you need to VOUT1 and VOUT1_I**individually** Go toVOUT1pathMOS Both ends (pictured below). It is worth noting thatVOUT1_IandVIObelong to the same network, but they must not be directly connected to the nearby network at will.VIO In the network, you must follow the specified pathMOSedge.

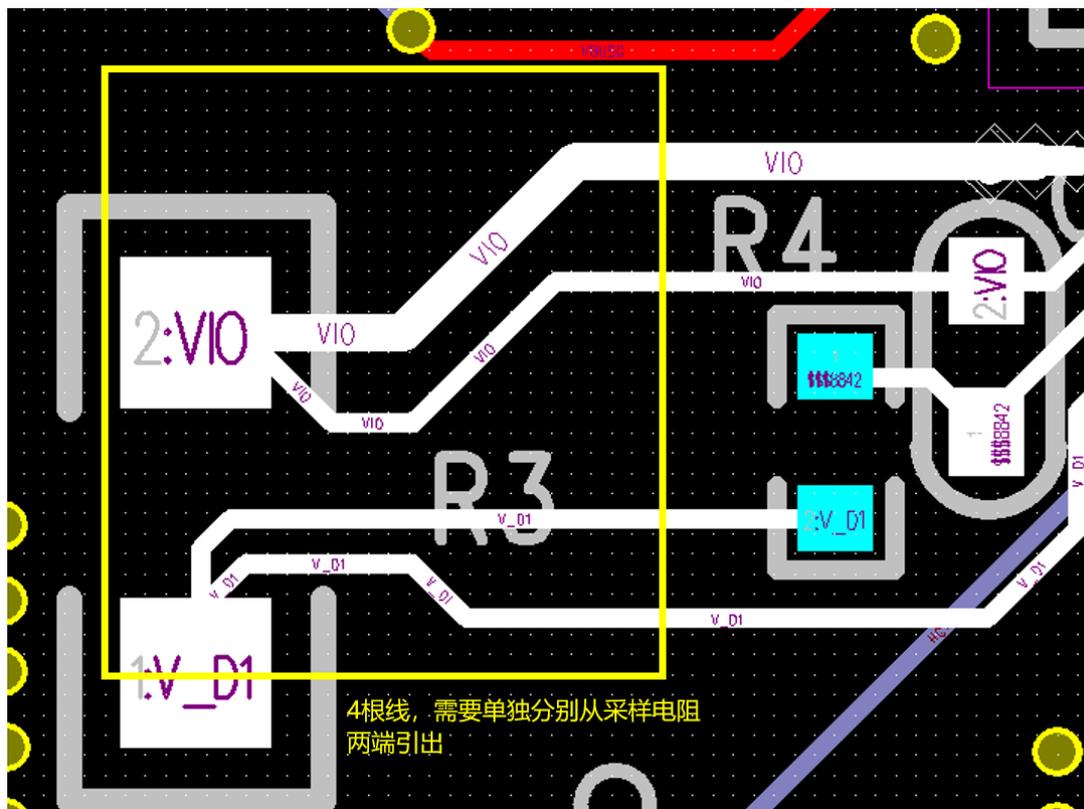


2.VIODuanheBATThe terminal sampling line needs to be led out from both ends of the sampling resistor separately, and the shorter the better.

In the schematic, the pinBAT,CSP2Belong to the same network, but when routing**must be separated separately**Lead from the right side of the sampling resistor;CSN2andPCONalso need**separately**Lead from the left side of the sampling resistor, as shown belowlayoutAs shown in the figure:

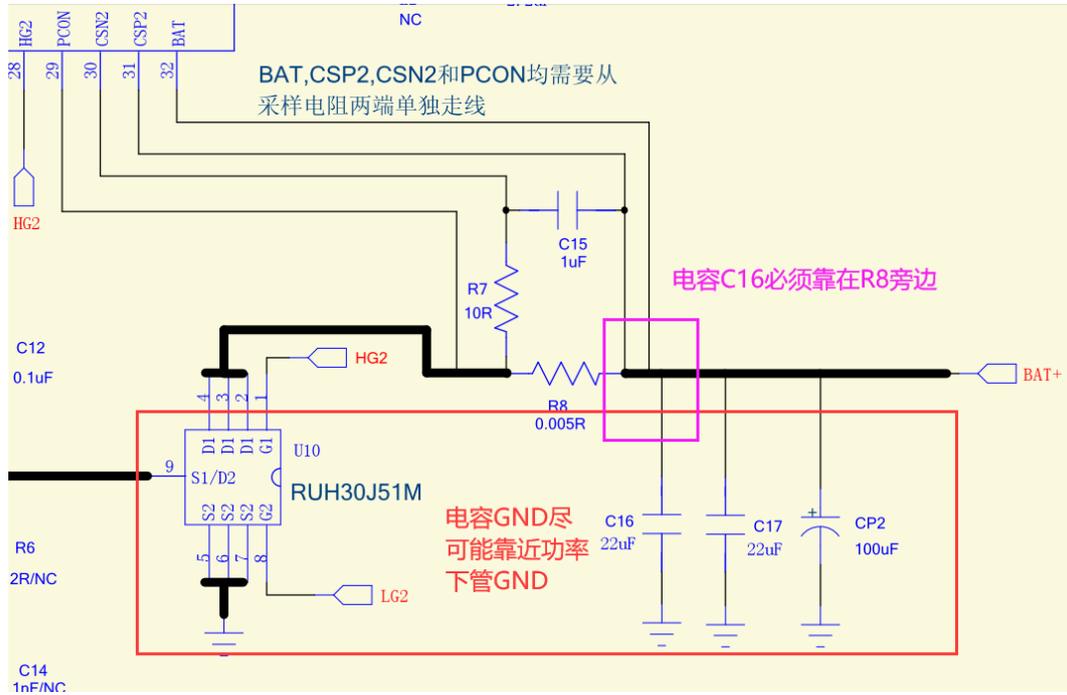


VIO The sampling resistor wiring at the terminal is the same:



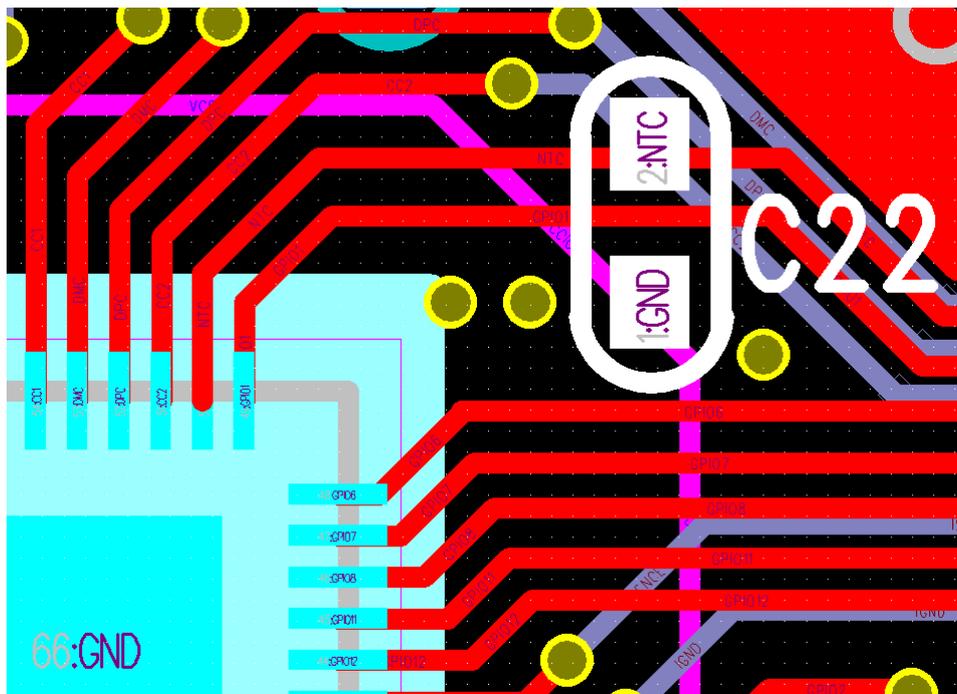
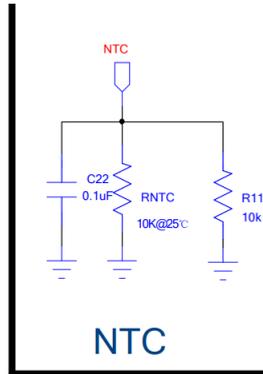
3.VIOandBATThe terminal capacitor needs to be close to the sampling resistor

byBATterminal, for example, next to the sampling resistor of this terminal **Must be at least** place a $22\mu\text{F}$ capacitance. Under this premise, try to make the capacitor as **GND** near BAT Under the power circuit **GND**, in addition **The more holes in the ground, the better**. Otherwise, the current may be ADC It affects the accuracy and stability of sampling.



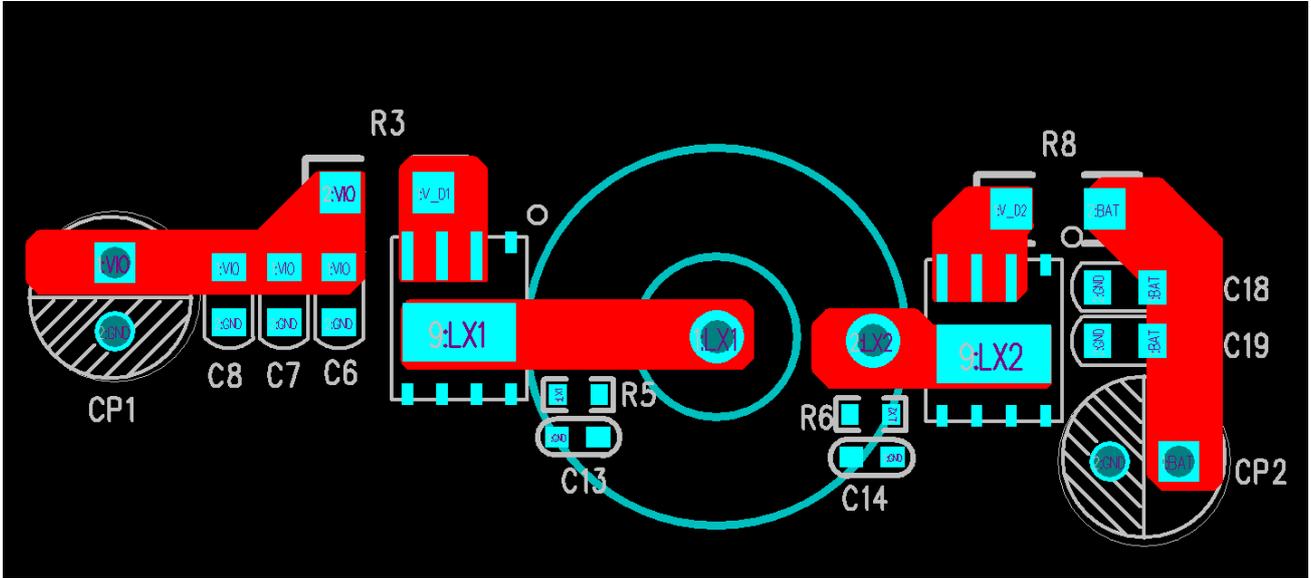
4.NTC The capacitor needs to be close to NTC pinfoot

accomplish NTC function used 0.1uF The capacitor needs to be close to IC of NTC pinfeet, as shown below layout As shown in the figure. In addition, no need NTC function, directly NTC pinFoot connection 10k just connect the resistor to ground, use NTC When using the function, you need to change the 10k resistance NC.

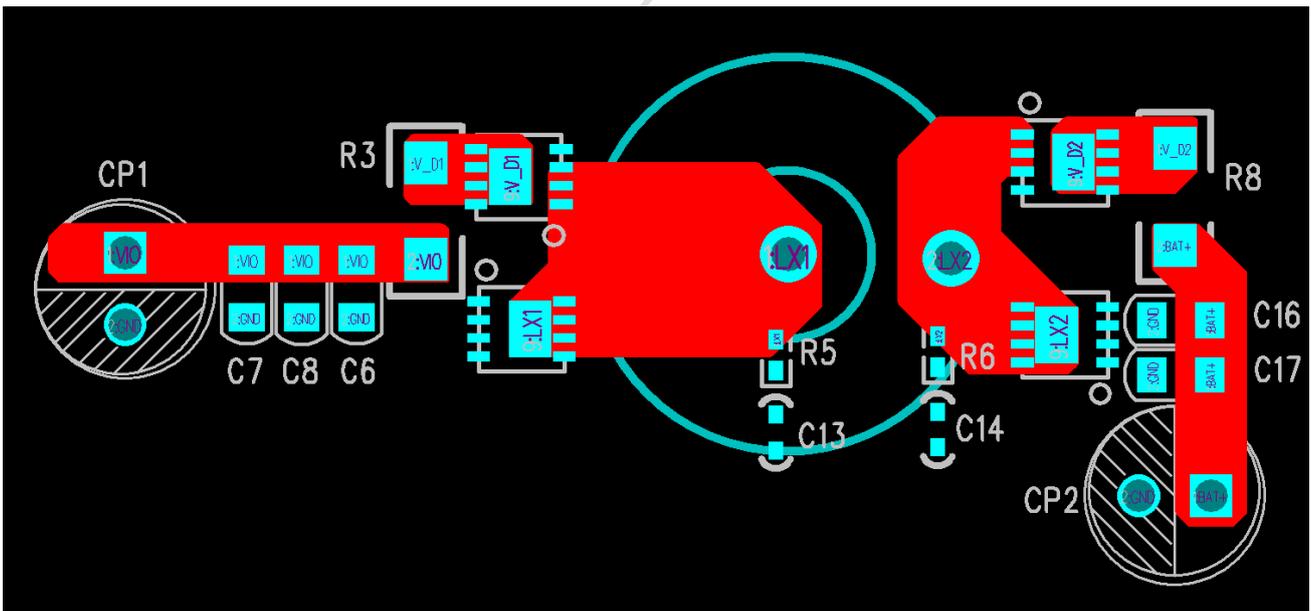


5. Power layout reference

For doubleNMOS and singleNMOS in both cases, we provide the following two layouts for reference:



pairNMOS Power layout reference



oneNMOS Power layout reference

No matter which solution is adopted, please ensure that the negative terminal of the capacitor and the negative terminal of the power tube are sufficiently connected.

enough holes. In addition, in order to deal with ringing interference at the moment of switching, we recommend adding RC Absorption loop. R is the specific value needs to be determined according to the actual device model and layout. The larger C is, the peak absorption capacity can be increased, but the consequent reduction in efficiency also needs to be considered. According to internal experiments, fixed R for 2R, at full load (100W) discharge situation, change C. The value of C, its impact on the maximum amplitude and efficiency of ringing is as follows:

R	C	Rings the most high amplitude	efficiency	efficiency ratio value, with 2R1nF for benchmark

2R	10nF	42	0.9539	0.9945
2R	6.8nF	43.4	0.9558	0.9964
2R	3.3nF	44.4	0.9576	0.9983
2R	1nF	52.6	0.9592	1.0000

The amplitude of the ringing is positively related to the size of the output current, so it is necessary to choose the appropriate one according to the actual application scenario. RC Take value. We recommend that if $P_{MAX} \leq 65W$, we recommend 2R 1nF of RC combination if $P_{MAX} > 65W$, we recommend 2R 3.3nF The combination.

three, IP5389 Summary of frequently asked questions

Device selection related issues:

1, IP5389 right H-bridge MOS driver requirements, such as Ciss capacitance value, etc., how to match to achieve the highest efficiency.

Generally speaking, for H-bridge NMOS, our recommended parameters are as follows:

Various attributes	Recommended parameters
$r_{DS(on)}$	<10mR
V_{GS}	$\geq 30V$
V_{DS}	$> 8V$
I_{DS}	$> 15A$
C_{iss}	<1000pF
t_{on}	<10ns
t_{off}	<40ns

H-bridge NMOS The main factors affecting efficiency include $r_{DS(on)}$, and C_{iss} , IP5389 of The driving voltage comes from VCC5V, the impact of parasitic capacitance on efficiency is relatively small, the main consideration is $r_{DS(on)}$ The smaller the better, in $r_{DS(on)}$ not much differences In this case, the smaller the parasitic capacitance, the better. Regarding the turn-on and turn-off times, the shorter the better.

2, the path of each input and output port NMOS How to choose

Each input and output port NMOS just as a path switch, the requirements for switching speed are not high. About on-resistance $r_{DS(on)}$, we recommend the path NMOS of $r_{DS(on)} < 10mR$, the smaller this value, the higher the overall efficiency. Resistant The pressure needs to be selected according to the actual situation. For example, the selected solution supports the highest 20V charge and discharge, then the path NMOS of The withstand voltage needs to be greater than 20V (Taking into account the margin, it is recommended to be greater than 25V); if the selected solution supports the highest 15V charge and discharge, then the path NMOS of The withstand voltage needs to be greater than 15V (Taking into account the margin, it is recommended to be greater than 20V).

3, How to select an inductor and why it is recommended to use it? 10uH of inductance?

We default IP5389 The power loop output current is , the switching frequency is , Input voltage , the output voltage is . According to the specific requirements of the project, we set the above parameters as follows:

Various parameters	value
-	5V
-	20V
-	5V
-	20V
-	250kHz
-	6A

Inductor ripple current Generally take 0.2 ~ 0.4, take here 0.3 times, then = 1.8A, the inductor current calculation formula is as follows:

$$I_{(igh)} = I_{(avg)} + 0.5 \Delta I$$

$$I_{(l)} = I_{(avg)} - 0.5 \Delta I$$

Calculated $I_{(igh)} = 6.9A$, $I_{(l)} = 5.1A$.

exist BUCK In working mode, let $V_{(in)} = 20V$, $V_{(out)} = 5V$, $I_{(avg)} = 1.8A$, neglect NMOS On-resistance, we get

The following formula:

$$L_{(min)} = \frac{V_{(in)} - V_{(out)}}{2 \Delta I} \times \frac{1}{f_{(s)}}$$

Calculated $L_{(min)} = 5.6\mu H$,

exist BOOST In working mode, let $V_{(in)} = 5V$, $V_{(out)} = 20V$, $I_{(avg)} = 1.8A$, also ignored NMOS On-resistance,

The following formula is obtained:

$$L_{(min)} = \frac{V_{(in)} \times (V_{(out)} - V_{(in)})}{2 \Delta I \times V_{(in)}} \times \frac{1}{f_{(s)}}$$

we got $L_{(min)} = 8.3\mu H$.

The inductance value of the inductor is 10uH that's it. The rated current depends on the specific usage scenario. Generally speaking, it needs to be at least greater than 7A, the lower the DC impedance, the better.

4. CSP2/CSN2 and CSP1/CSN1 What is the sampling accuracy value of the feet? Resistors and voltages on the sampling circuit

What is the function of capacity and how to obtain its value?

The accuracy of current sampling is affected by the internal trimming reference, sampling amplification matching error, and the accuracy of the sampling resistor itself. PCB The influence of wiring and soldering effects. Without considering the error of the sampling resistor, the sampling accuracy can only be guaranteed to be <2%, to achieve higher accuracy, it is necessary to test the actual deviation during normal operation after the placement is completed, and modify the coefficient of the deviation through software.

We did a random test 3 piece IP5389 of demoboard, the average current sampling accuracy is within 1.3% about. The resistor and capacitor on the sampling circuit function as a low-pass filter, and the resistance value is selected 10R, capacity value selection 1uF, then the cutoff frequency of the filter is 16KHz, and the switching frequency (250kHz) difference 16 times, mainly to filter the switching ripple of the sampling current.

5. How to choose the capacitance value of the two bootstrap capacitors?

exist IP5389, the power supply source of the bootstrap capacitor is VCC5V, in order to conduct the upper tube BST. The stability of the capacitor voltage generally needs to meet the $\Delta V > \gg$, in most cases, $\Delta V = 100$ mV, exist H-bridge MOSFET. Does not exceed 1nF (1000pF), the conventional value is 100nF (0.1uF).

6. What is the appropriate amount of filter capacitor for each input and output port?

Generally speaking, it is recommended that the capacitance value of the output port should not exceed 22uF, otherwise it may affect EMI. The certification has been passed, and excessive output capacitance may cause problems such as false triggering of load detection. So we recommend using 10uF, you can also connect an additional one in parallel 0.1uF capacitance to reduce EMI interference.

7. In practical applications, CC/D+/D- Generally, some resistors and capacitors are added. What are their values?

Formula?

In most cases, in CC/D+/D- The purpose of adding resistors and capacitors is to pass some certifications. The specific values are not easy to calculate theoretically. There are many factors that affect these parameters, such as PCB layout and wiring often need to be adjusted step by step on the actual board to finally get the appropriate parameters.

8. VIO and BAT How to choose the capacitor? why VIO recommend 3 individual 22uF in parallel 1 individual 100uF, and

BAT recommend 2 individual 22uF in parallel 1 individual 100uF.

According to the capacitance calculation formula of switching power supply, VIO and BAT. The minimum capacitance value of the capacitor is 100uF, and considering that in actual use, the capacitance value may decrease as the use time increases or the temperature rises, we conducted a large number of charge and discharge experiments, and finally concluded VIO use 3 individual 22uF and 1 individual 100uF, BAT use 2 individual 22uF and 1 individual 100uF. Recommended parameters of the capacitor. In addition, the capacitance of each output port is only used for simple filtering. 10uF that's it.

Questions related to system functions:

1, What is the load detection triggering logic of the port, and what is the detection voltage/load current value?

The port relies on detecting voltage to judge the load. When the port is in standby, it will pass VOUT1/VOUT2 release one 2.4V. The detection voltage reaches A1/A2 port, the load capacity is 5uA, when the load (equivalent impedance <math><400k\Omega</math>) is inserted, the output port's 2.4V will be pulled low quickly when the voltage is below the trigger detection threshold (2.0V), it is determined that a load is inserted.

When performing single-port fast charging and discharging, if the other output port detects load insertion, it will switch to dual-port at this time. When it is detected that the current of an output port is less than the single port light load threshold (currently this value is 80mA, default path NMOSthe internal resistance is 10mR), the output port will be closed, and then the remaining port can resume the fast charge and discharge state.

2, How should the selection be used?

Select function by changing FCAP. The resistance value of the resistor to ground is used to configure different capacities. The capacity here refers to **single series battery** capacity, for example, 4Festiva15000mah cells are connected in series, then the capacity configuration is 5000mah, the resistor is set to $5000/0.8=6.2k$. That's it; 4Festiva15000mah The battery cells are connected in parallel and then in series, then the number of cells here is selected 2 section, the capacity is configured as 10000mah, the resistor is set to $10000/0.8=12.4k$.

3, In the application, how should the power and number of nodes match the settings? 2 Sections can be configured 100W?

Since the current limit value is maximum 8.8A, and the shutdown voltage of currently supported lithium iron phosphate batteries is 2.75V, 2Lithium iron phosphate battery under low power output BATThe maximum terminal power is $2.75V * 2 * 8.8A = 48.4W$, considering efficiency, VIOThe end is unsatisfactory 45W discharge, and in this case, BATThe current is very large and the heat will be serious, so we recommend 2When saving battery, PMAH Highest configuration 30W. In the same way, 3When saving battery, we recommend PMAH Highest configuration 45W. Only when the number of batteries is greater than 4During the holidays, we only recommend $P_{MAX} >= 60W$.

Additionally, configure PMAH for 65W and 100W need extra Mark Circuit, please refer to the schematic diagram for the specific circuit.

4, What power does it refer to, for example, configured as 45W, AThe port can output 45W?

PMAH Refers IP5389 of VBUS or VIN The maximum input and output power supported, VOU T The maximum port only supports output 24W, at present IP5389 No I2C The program can only set the same input and output power. If you need to set different output and input powers, please use I2C plan.

when PMAH configured as 45W hour, VBUS support 45W Discharge. exist VIN as DCIn the plan of the mouth, VIN The maximum input power is also 45W, AThe port output power is still 24W.

5,VBUSmouth andVINWhen do I need to use a pair of tubes?

Normally VBUS mouth and VIN O rally use one N MOS It can be used as a path tube. When using it, pay attention to the source (S) toward the seat end, the drain (D) towardsVIOJust end it.

Generally, you need to set up peer management in the following two situations:

- 1, when passing some certifications, you need toVBUSThe port is set to the opposite tube to achieve the effect of complete shutdown in both directions. When using
 When using a pair of tubes, we recommend starting from the seat toVIOEnd according toD1---S1---S2---D2Place the pairs of tubes in this order. 2,
 when there is a mouthfulDCWhen inputting, you need to set the corresponding pipe in the port path. hypothesisVINmouth actDCenter,VBUS
 asType-Cport, if no pairing pipe is set, inVINWhen high pressure is inserted into the port, the high pressure will pass throughNMOSThe
 parasitic diode sinks directly into IP5389 of VIO on the path, if at this timeType-CGiving mobile phone5VCharging, because it cannot be
 turned off in timeVBUSpath pipe, this high pressure will also be poured intoVBUS, which is likely to cause damage to the phone.

6,IP5389Can I freely burn firmware of different models?

Currently, it is not possible to burn firmware across models. One model IP5389 You can only burn the firmware of this model, and cannot burn the firmware of other models. When trying to use the burner we provide for cross-model burning, you will find that you cannot connect. You can also judge from this point IP5389 Whether it is the same model as the provided firmware.

7, can you describe it in detail?VINasDCWhat is the strategy for drawing current from the input port?

VINasDCInput port, equipped withMPPTFunction, its external power supply is generally divided into two categories:1, solar panel output; 2,DC output adapter. IP5389 will passtry To distinguish the above two types of power sources by the way of current, if it is a solar power source, ICIt will intelligently adjust the current extracted according to the output power of the solar power source in real time to maximize energy utilization. The current drawn is defined asIset,scope0~5A,DCWhen the mouth is working normally,Vsysneed to be satisfied4.5V~25V.

The following areVINoralMPPTFlow chart:

9, why I set the battery to end4section, battery full voltage 4.2V, set up VBAT=13V Power on, activate

After the battery is still 0?

Currently, when powering on for the first time, if $V_{BAT} < N * 3.4V$, the electric power is judged as 0. The main reason is that if the lithium battery is shut down early during the last high-power discharge, then after the subsequent charging is activated, the battery will suddenly become a very low battery. In order to avoid this situation, the above modifications were made.

10. In the actual application process, can some output ports be deleted? If so, what are the corresponding output ports?

Function pin How should this be handled?

can delete.

If the deleted output port is USB-A port, for example, in an application, there is no need for USB-A2 mouth, then with USB-A2 related V_{OUT2} , $DPA2$, $DMA2$, V_{OU2_I} it will have no effect. At this time, you can IP5389 of V_{OUT2} , $DPA2$, $DMA2$ pin is left floating while V_{OUT2_I} you need to pass a 10k Pull up to V_{CCIO} .

If what is deleted is Type-C mouth or Micro-B port, then the corresponding pin can be left floating directly.

11, use I2C version of the program, why IP5389 Still unable to shut down?

First check against the register document IP5389 registers related to light load shutdown to see if the light load shutdown function is turned off; then check whether the light load shutdown function is turned off. INT It has been pulling higher and is currently at IP5389 When it's time to shut down, IC will INT Configured as an input to detect external voltage, if it is high, it will not shut down.

12, whether it is possible to add an exception IO mouth, in IP5389 Notify in time when exceptions occur MCU?

at present IP5389 of INT There is this function, please refer to IP5389 register file, in $0xEA0F0x0C$ of bit 6, which can be written as 1, followed by IP5389 When an exception occurs, INT every interval 500ms It will be pulled down once, every time 1~2ms. MCU It can be judged by detecting this signal IP5389 Whether an exception occurs.

13, why C The port output only supports high voltage SCP?

exist IP5389 of C When charging a Huawei mobile phone through the port, the mobile phone applies first PD High pressure agreement, will apply later SCP, so we will give priority to using high voltage SCP Agreement to charge Huawei mobile phones.

We turned it down C Low voltage at port output SCP priority, deleted C Port output supports low voltage SCP instruction of. but not removed C low pressure in mouth SCP Output function, so when using a decoy device for testing, you can also decoy low voltage alone. SCP of.