



P-Channel Enhancement Mode Power MOSFET

Description

The 9435 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V.

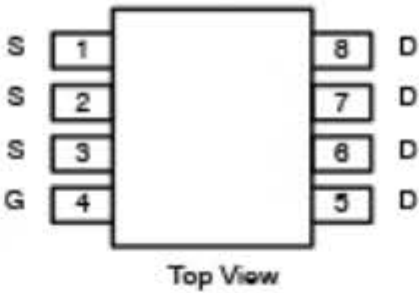
General Features

- $V_{DS} = -30V$
 $R_{DS(ON)} < 85m\Omega @ V_{GS} = -4.5V \quad I_D = -4.2A$
 $R_{DS(ON)} < 57m\Omega @ V_{GS} = -10V \quad I_D = -5.3A$
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

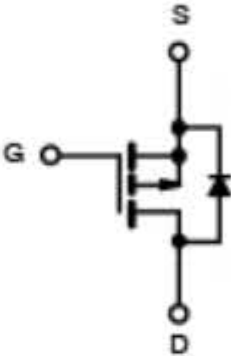
Application

- Battery Switch
- Load switch
- Power management

SOP-8



Equivalent Circuit



MARKING



Y :year code W :week code

Absolute Maximum Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ($T_J = 150^{\circ}C$)	$T_A = 25^{\circ}C$	I_D	-5.3	A
Drain Current-Pulsed (Note 1)		I_{DM}	-20	A
Maximum Power Dissipation		P_D	2.0	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 To 150	$^{\circ}C$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	50	$^{\circ}C/W$
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Electrical Characteristics (T_A=25℃ unless otherwise noted)

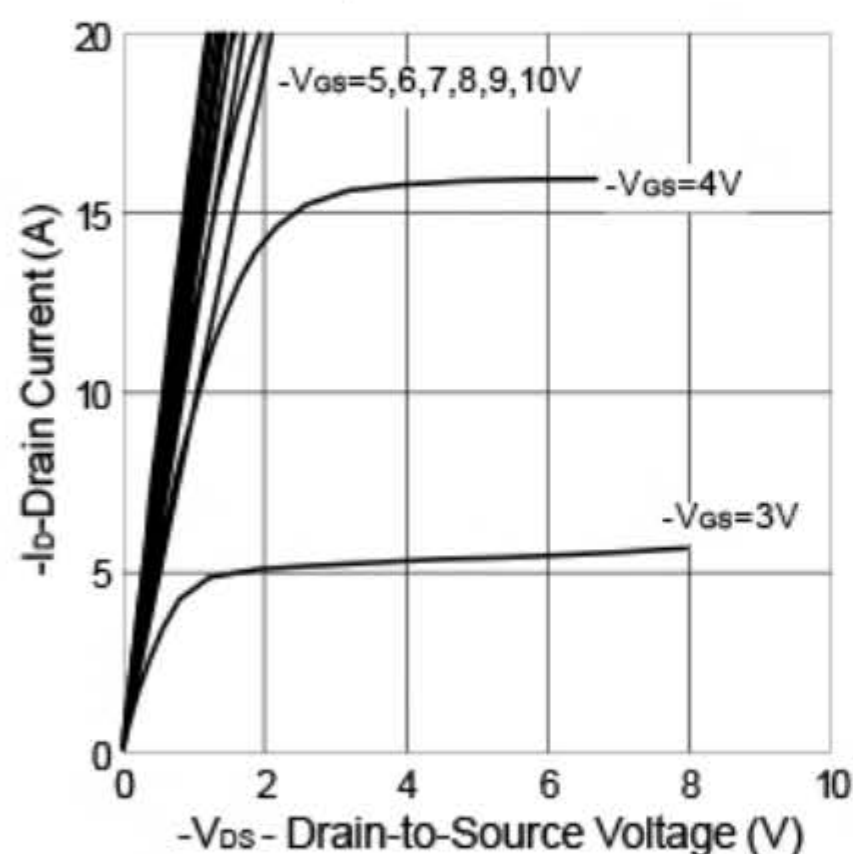
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-30	-33	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1	-	-3.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-5.3A	-	51	57	mΩ
		V _{GS} =-4.5V, I _D =-4.2A	-	75	85	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-15V, I _D =-5.3A	10	-	-	S
Dynamic Characteristics ^(Note4)						
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V, F=1.0MHz	-	845	-	PF
Output Capacitance	C _{oss}		-	120	-	PF
Reverse Transfer Capacitance	C _{rss}		-	80	-	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-25V, I _D =-2A, V _{GS} =-10V, R _{GEN} =6Ω R _L =12.5Ω	-	17	-	nS
Turn-on Rise Time	t _r		-	18	-	nS
Turn-Off Delay Time	t _{d(off)}		-	60	-	nS
Turn-Off Fall Time	t _f			27	-	nS
Total Gate Charge	Q _g	V _{DS} =-15V, I _D =-4.6A V _{GS} =-10V	-	22	-	nC
Gate-Source Charge	Q _{gs}		-	4.5	-	nC
Gate-Drain Charge	Q _{gd}		-	2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V, I _S =-2.0A	-	-	-1.2	V

- Notes:**
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
 - 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
 - 3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
 - 4. Guaranteed by design, not subject to production

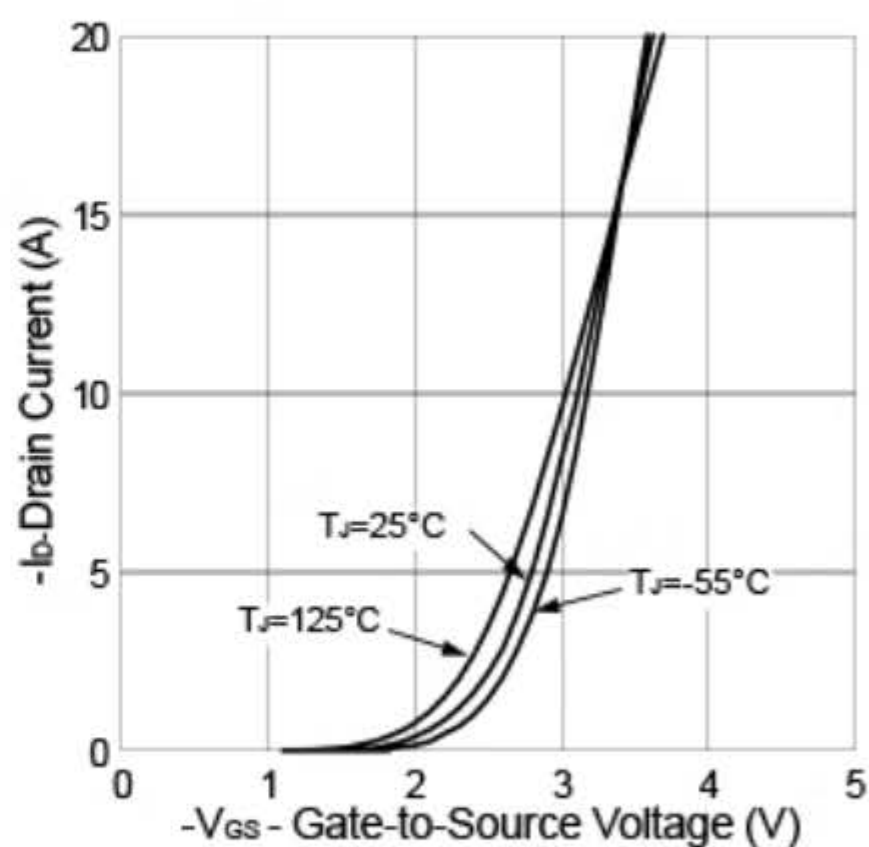


Typical Characteristics

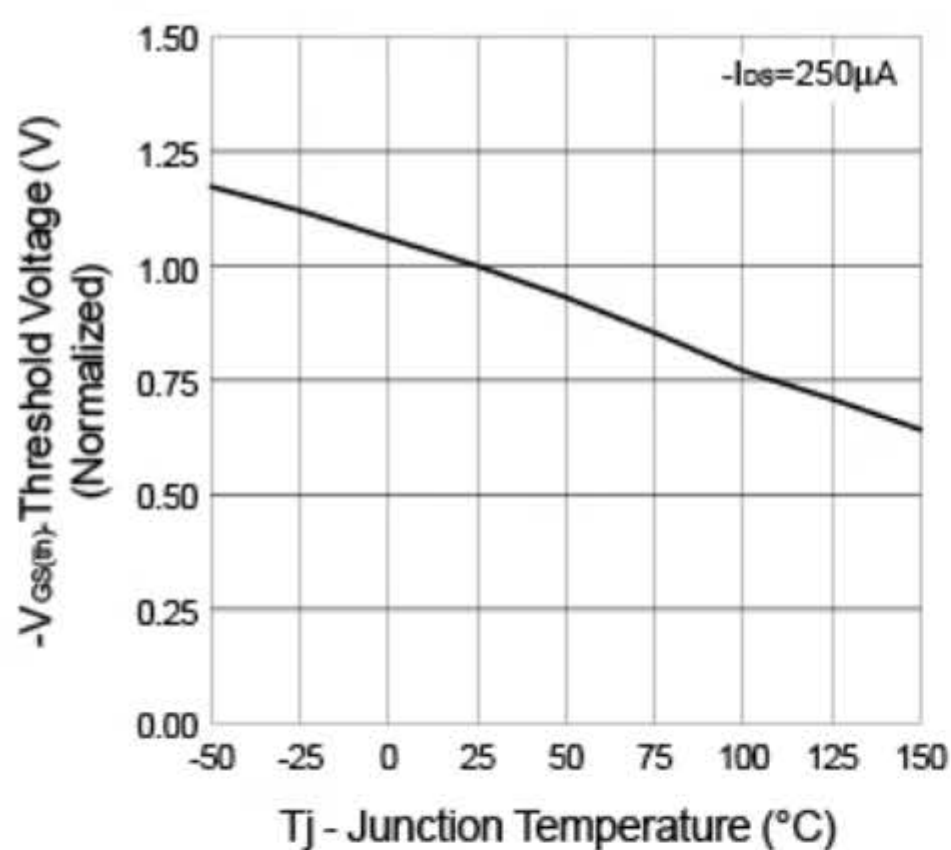
Output Characteristics



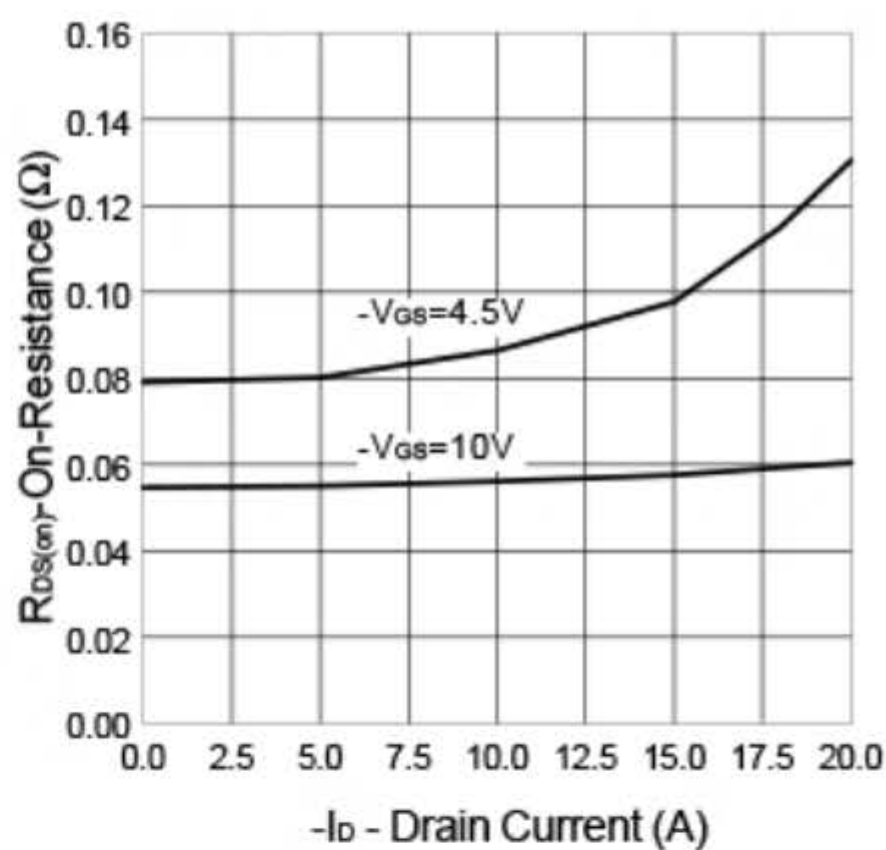
Transfer Characteristics



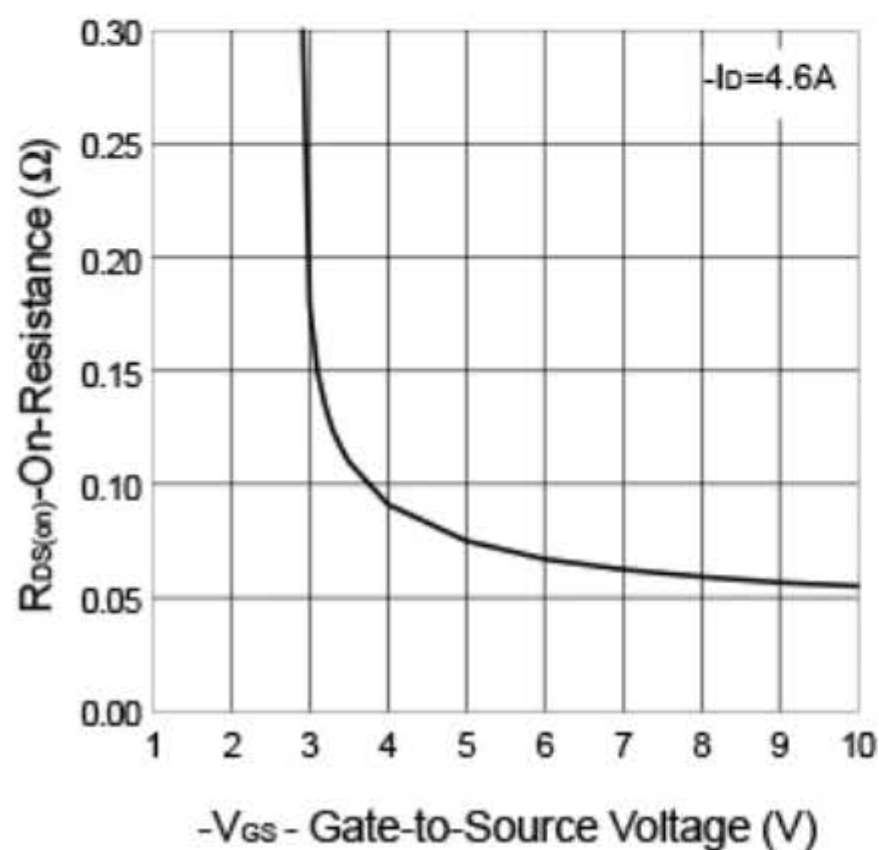
Threshold Voltage vs. Junction Temperature



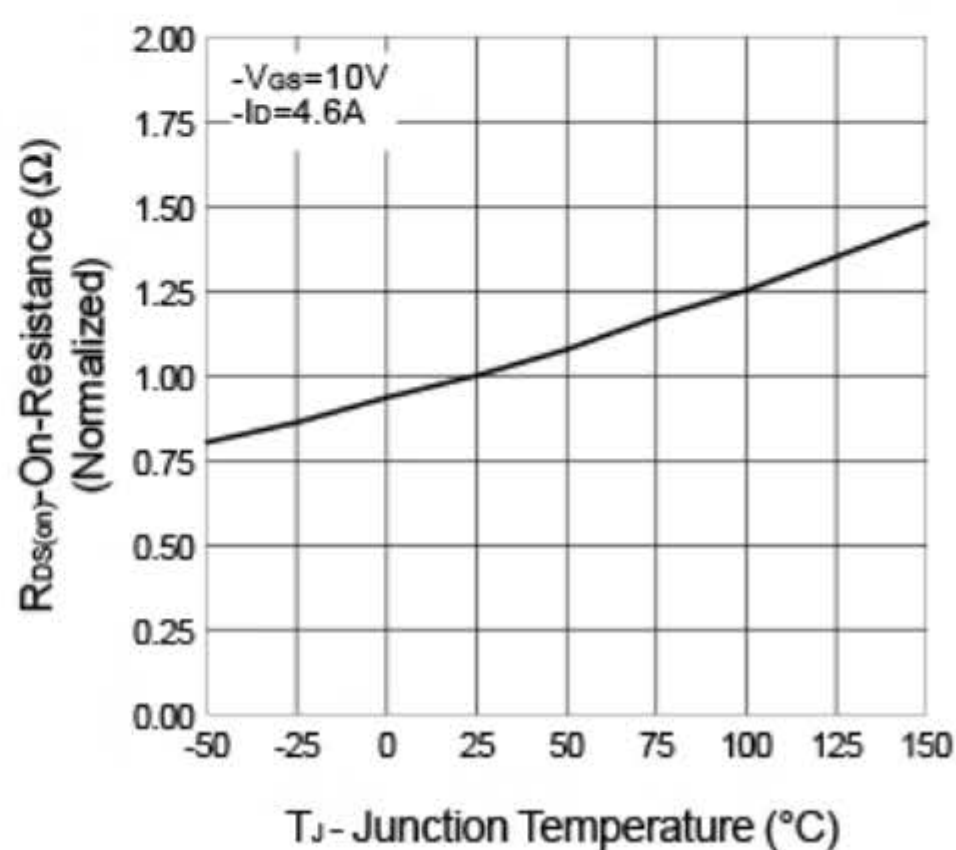
On-Resistance vs. Drain Current



On-Resistance vs. Gate-to-Source Voltage

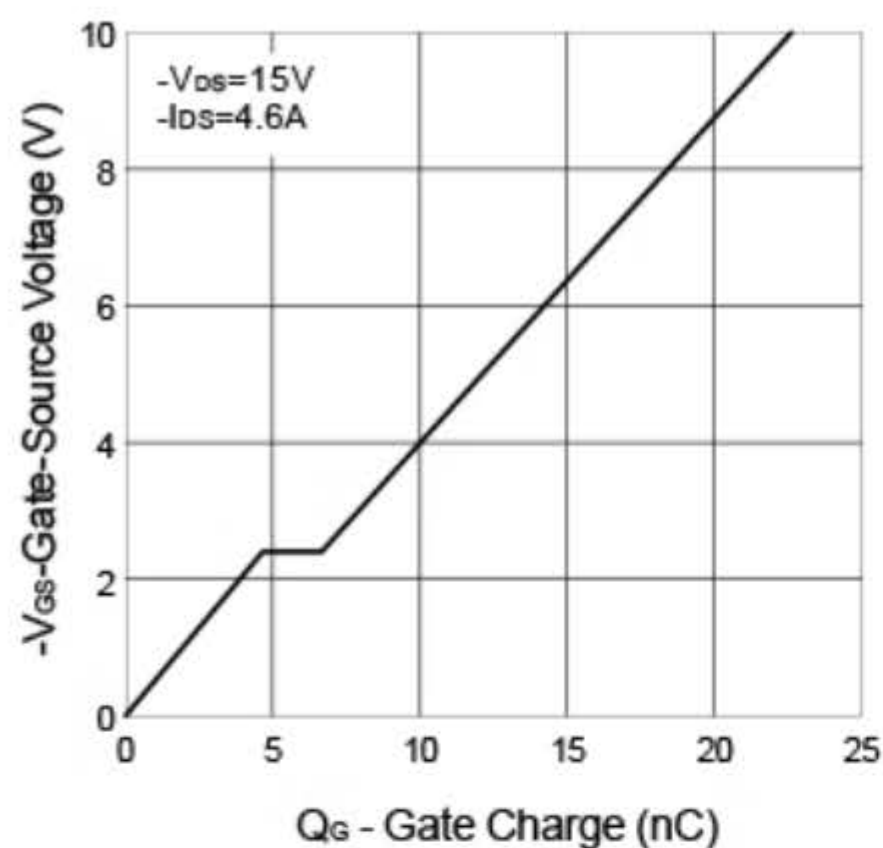


On-Resistance vs. Junction Temperature

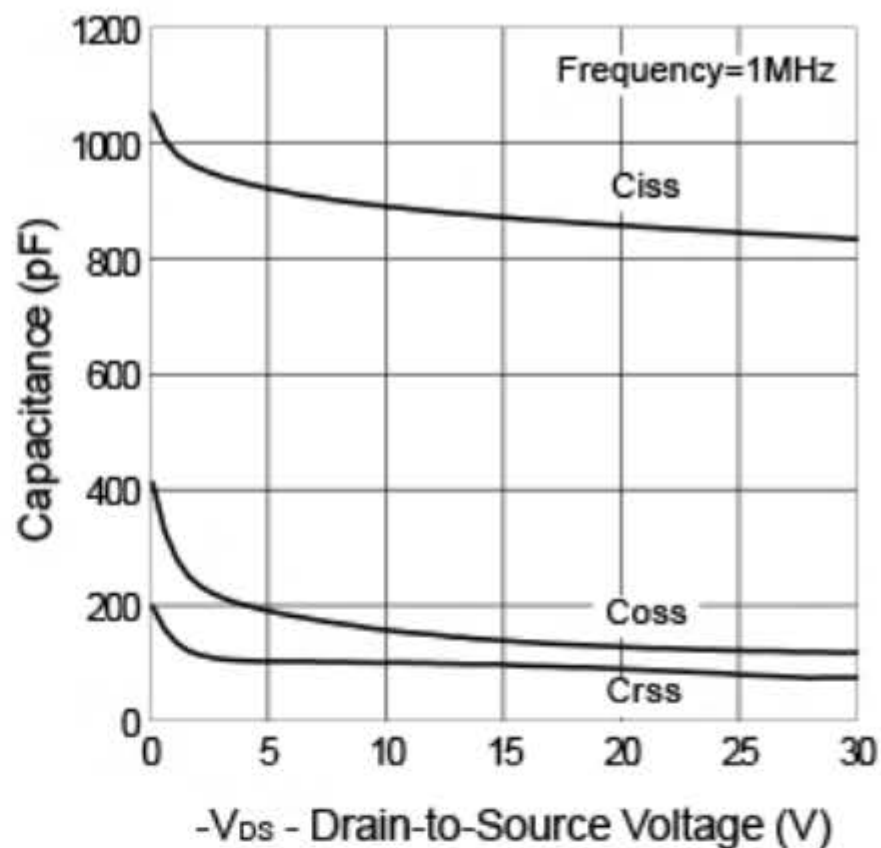




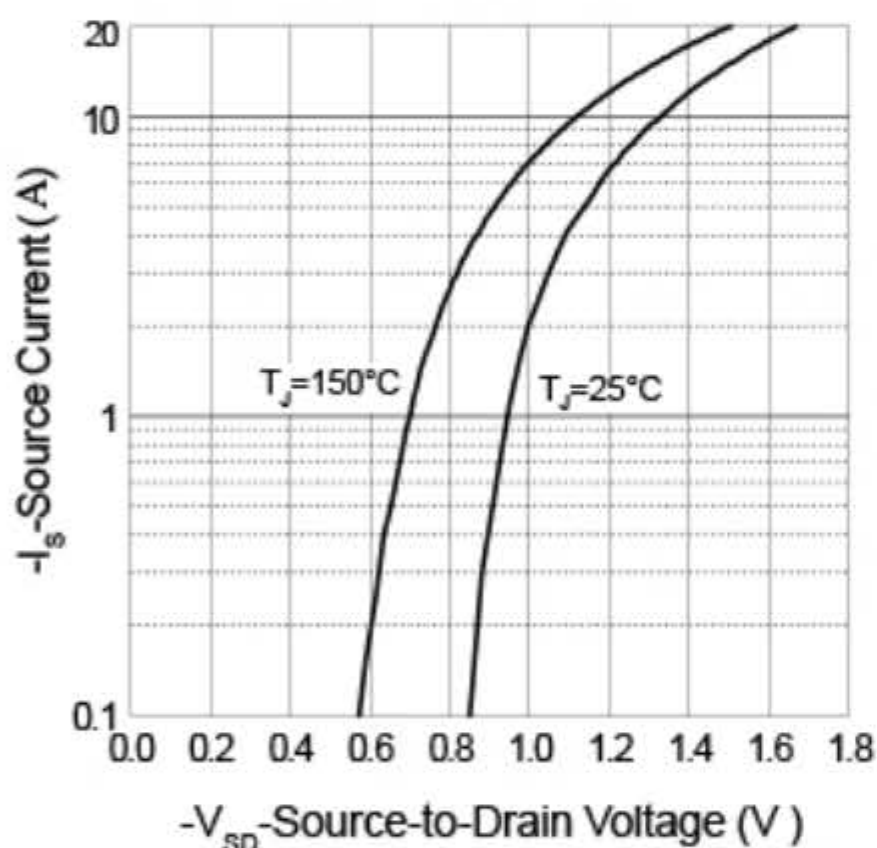
Gate Charge



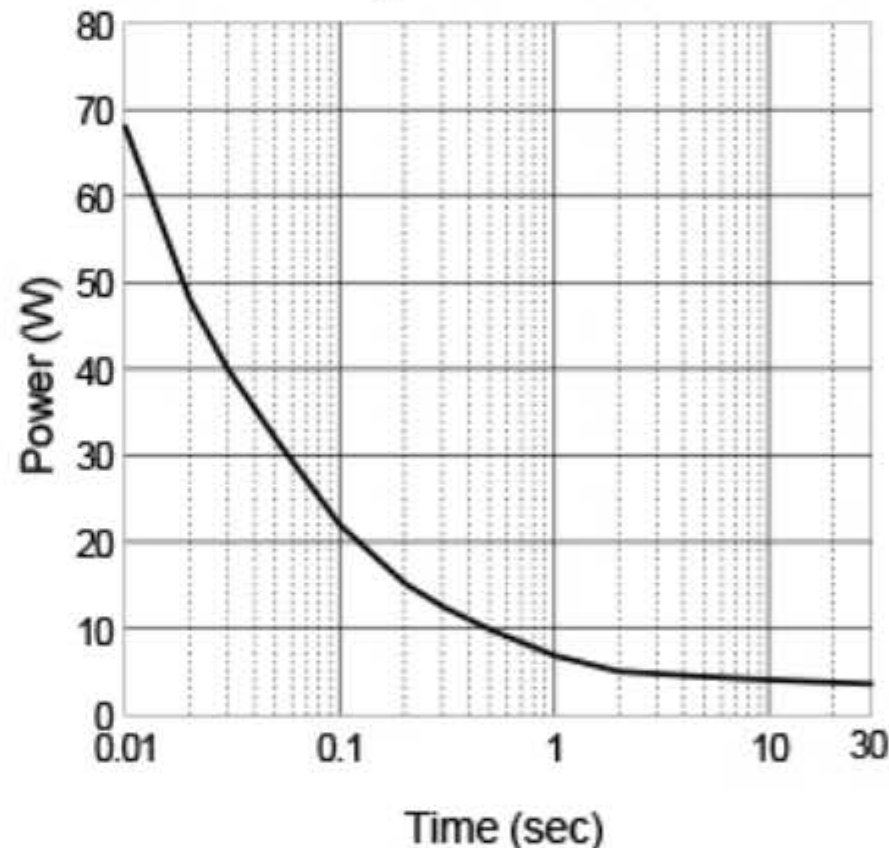
Capacitance



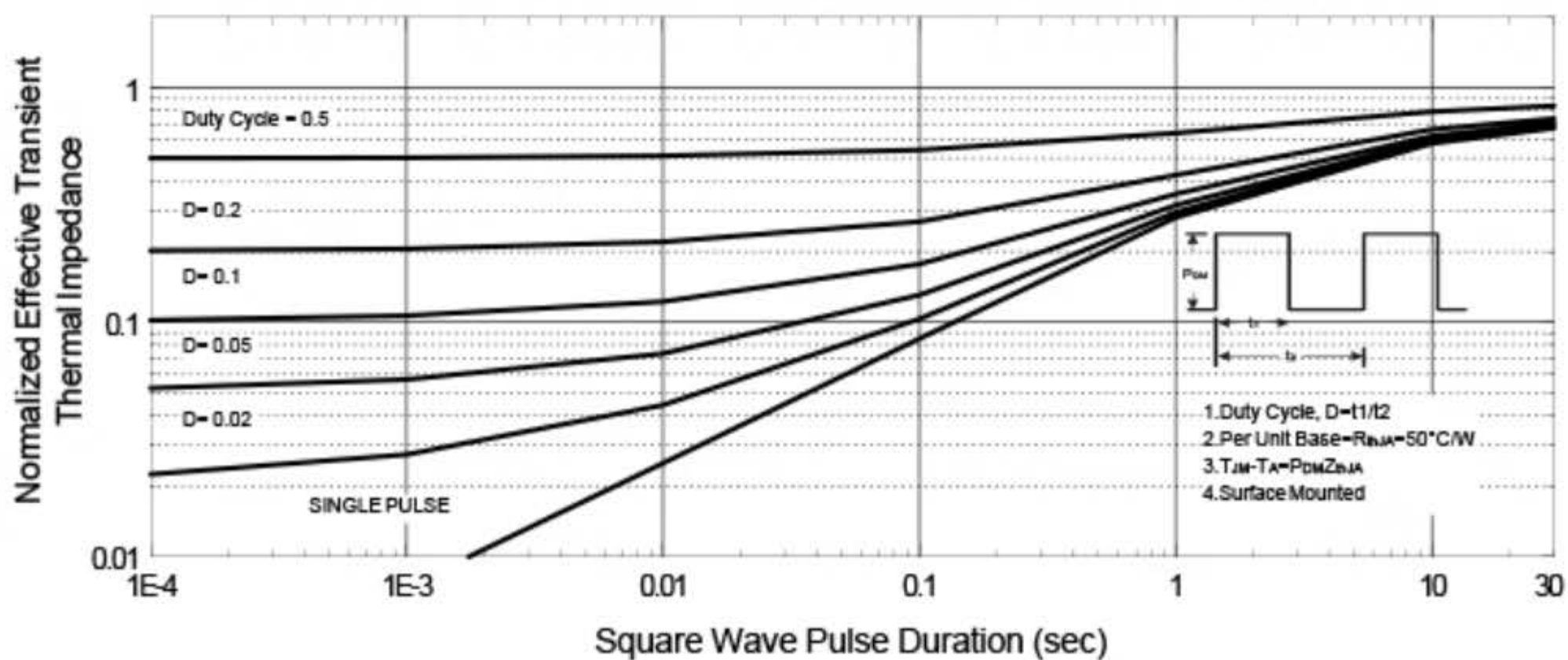
Source-Drain Diode Forward Voltage



Single Pulse Power



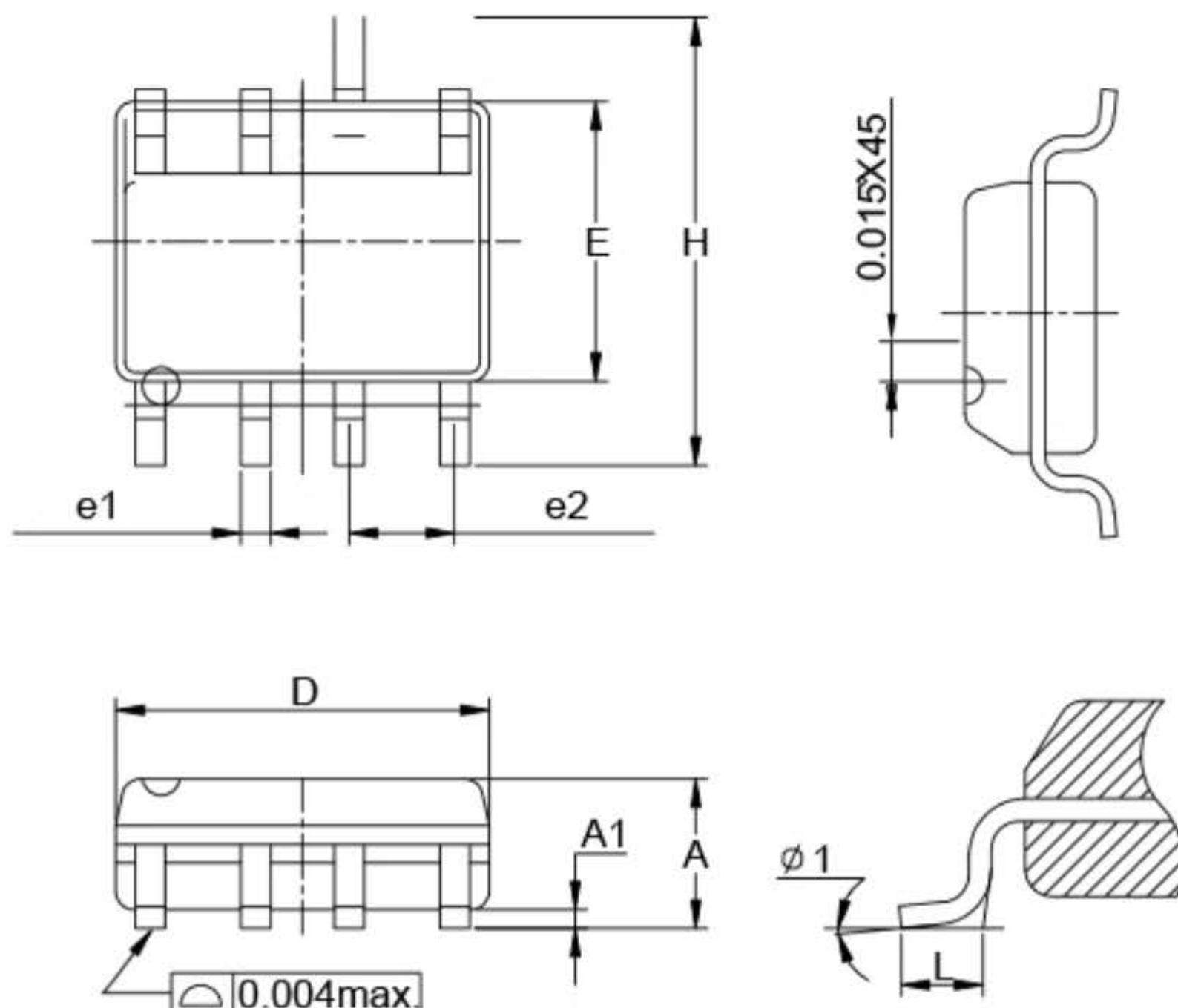
Normalized Thermal Transient Impedance, Junction to Ambient





Packaging Information

SOP-8 pin (Reference JEDEC Registration MS-012)



Dim	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.80	5.00	0.189	0.197
E	3.80	4.00	0.150	0.157
H	5.80	6.20	0.228	0.244
L	0.40	1.27	0.016	0.050
e1	0.33	0.51	0.013	0.020
e2	1.27BSC		0.50BSC	
Ø 1	8°		8°	